**Call for Papers on ICCMIT 2017**

**“Current Trends in Analog Signal Processing Circuits”**

**Organized by:**

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**Objectives and Motivation**

It is now fairly well understood by academicians as well as practice engineers and circuit designers that in spite of the dominance of digital circuits and systems and digital signal processing, the importance of analog signal processing and analog circuits has not diminished by any account. So, research on analog circuit design has continued to remain prominent although, the design considerations and focus have changed from time to time alongside the changes in integrated circuit (IC) technology.

Therefore, this special session entitled “Current Trends in Analog Signal Processing Circuits” is mainly aimed to cover the recent innovations, trends, practical challenges and possible solutions in the field of modern analog signal processing circuits and their COMS/BJT implementations, analog integrated circuits, mixed signal designs and analog VLSI/Microelectronics circuits.

**Scope and Interest**

In several areas and applications, analog circuits are indispensible e.g. continuous time filters, amplifiers, precision rectifiers, analog-to-digital (A/D) converters, digital-to-analog (D/A) converters and several classes of communication, instrumentation and measurement circuits.

Topics: The topics of interest include but are not limited to:

* Current Mode/Voltage Mode/Mixed Mode Analog Signal Processing Circuits
* Advance Analog Integrated Circuit Blocks
* CMOS Realizations of Modern Active Building Blocks.
* Analog Signal Generation Circuits
* Digitally Controlled Active Building Blocks
* Synthesis of Signal Processing/Generation Circuits
* Active Realizations of Immittance Functions
* Hardware Implementations of Modern Active Building Blocks
* Semiconductor Device Design, Modeling, Processing And Characterization
* Low Power VLSI Circuits
* Single Electron Devices
* Recent Advancements Of CMOS Technology
* Device-Circuit Co-Design
* Passive Component Modeling
* High Electron Mobility Semiconductor Devices
* Analog Memories
* Analog Layout Area Optimization
* Microelectronic circuits

**Paper Submission Important Dates.**

**All instructions and templates for submission can be found in the ICCMIT 2017 web site:** [**http://www.iccmit.net/**](http://www.iccmit.net/)**. Please, contact the special session organizers if you are planning to submit any paper.**

**Paper abstract submission: February 15, 2017**

**Notification of acceptance: February 28, 2017**

**Final paper submission and authors camera ready: March 7, 2017**

**Conference Dates: April 3-5, 2017**