Call for Papers for ICCMIT-2018
April 2-4, 2018, Madrid, Spain

Special Session
on
“Hardware Security and Authentication for High Speed Signal Processing Architectures”
Organized by:
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Objective and Motivation
Today’s world electronics devices are increasingly interconnected and play an important role in people’s lives for security, privacy protection and trustworthy computing. These devices working on digital signal processing such as, portable systems, wired/wireless communication, biomedical signal processing. The privacy protections of corporate or individuals have emerged as important challenge for this decade. Corporate are investing Millions of dollar for making their systems more secure against different attacks.

Scope and Interest
The objective of this special session is to provide a platform to researchers and industry people to discuss the challenges faced while designing the optimized VLSI architecture for signal processing applications. Hardware security has become one of the main challenges with Power and Area for designing new architecture. In this session focus will be made on different security algorithms with their prototyping development for the hardware. Following topics are more interested also we are ready to accept other related issues related to this

2. VLSI Architecture for Signal Processing
3. Low Power VLSI Design
4. Security Challenges in IoT.
5. Computer Architecture Design
6. Hardware Authentication
7. FPGA prototyping design of new security algorithm.
8. ASIC Design for Secure Architecture.
Paper Submission

All instruction and templates for submission can be found in the ICCMIT2018 website: http://www.iccmit.net.

The accepted papers will be published in Scopus/ISI indexed journals.
The best articles will be invited to be published again after expansion as book chapter in IGI Books.
Papers must be submitted to editor@iccmit.net and jyotirmoy.16082@lpu.co.in

Important Dates:

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